

Refine Search

Search Results -

Term	Documents
MACRO	58856
MACROES	1
MACROS	8161
MACROE	3
INSTRUCTION	393399
INSTRUCTIONS	523734
REGISTERS	334138
REGISTER	711236
STACK	446022
STACKS	98092
PUSH\$5	0
((PUSH\$5 OR POP\$5) NEAR6 (MACRO OR MACRO NEAR1 INSTRUCTION) NEAR15 (REGISTERS) NEAR15 (MEMOR\$4 OR STOR\$4 OR STACK)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD.	9

There are more results than shown above. [Click here to view the entire set.](#)

Database:

US Pre-Grant Publication Full-Text Database
 US Patents Full-Text Database
 US OCR Full-Text Database
 EPO Abstracts Database
 JPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L31

[Refine Search](#)

[Recall Text](#)

[Clear](#)

[Interrupt](#)

Search History

DATE: Friday, November 03, 2006 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

Set
Name Query

Set
Hit Name

side by side	Count	result set
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L31</u> (push\$5 or pop\$5) near6 (macro or macro near1 instruction) near15 (registers) near15 (memor\$4 or stor\$4 or stack)	9	<u>L31</u>
<u>L30</u> L27 and 19	15	<u>L30</u>
<u>L29</u> L27 and 18	12	<u>L29</u>
<u>L28</u> L27 and 17	156	<u>L28</u>
<u>L27</u> (single or one) near10 (push\$5 or pop\$5 or move or exchang\$4) near6 (command\$1 or instruction or microinstruction or macro or macro near1 instruction) near15 (registers) near15 (memor\$4 or stor\$4 or stack)	299	<u>L27</u>
<u>L26</u> (single or one) near6 (command\$1 or instruction or microinstruction or macro or macro near1 instruction) near15 (registers) near15 (memor\$4 or stor\$4 or stack)	7506	<u>L26</u>
<u>L25</u> 117 and 19	6	<u>L25</u>
<u>L24</u> 117 and 18	2	<u>L24</u>
<u>L23</u> 117 and 17	149	<u>L23</u>
<u>L22</u> L18 and 9	148	<u>L22</u>
<u>L21</u> L18 and 8	148	<u>L21</u>
<u>L20</u> L18 and 7	148	<u>L20</u>
<u>L19</u> L18 and 17	149	<u>L19</u>
<u>L18</u> L17 and 17	149	<u>L18</u>
<u>L17</u> (push\$5 or pop\$5) near12 registers near12 (two or group\$4 or subgroup\$4 or double or dual or set\$1 or bank\$1) and fetch\$5 near50 instruction\$1 near5 decod\$3	227	<u>L17</u>
<u>L16</u> L13 and 19	6	<u>L16</u>
<u>L15</u> L13 and 18	1	<u>L15</u>
<u>L14</u> L13 and 17	88	<u>L14</u>
<u>L13</u> (push\$5 or pop\$5) near12 registers near12 (group\$4 or subgroup\$4 or double or dual or set\$1 or bank\$1) and fetch\$5 near50 instruction\$1 near5 decod\$3	157	<u>L13</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>		
<u>L12</u> 16 and 19	21	<u>L12</u>
<u>L11</u> 16 and 18	9	<u>L11</u>
<u>L10</u> 16 and 17	491	<u>L10</u>
<u>L9</u> (712/202,203)[CCLS]	264	<u>L9</u>
<u>L8</u> (711/129-132)[CCLS]	1115	<u>L8</u>
<u>L7</u> (712/2-300)[CCLS]	12745	<u>L7</u>
<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>		
<u>L6</u> (push\$5 or pop\$5) near6 registers and fetch\$5 near50 instruction\$1 near5 decod\$3	743	<u>L6</u>
<u>L5</u> L4 not 12	0	<u>L5</u>
<u>L4</u> (push\$5 or pop\$5) near12 registers near70 fetch\$5 near50 instruction\$1 near5 decod\$3	42	<u>L4</u>
(push\$5 or pop\$5) near12 registers and fetch\$5 near50 instruction\$1 near5		

<u>L3</u>	decod\$3	910	<u>L3</u>
<u>L2</u>	(push\$5 or pop\$5) near12 registers near35 fetch\$5 near50 instruction\$1 near5 decod\$3	42	<u>L2</u>
<u>L1</u>	(push\$5 or pop\$5) near12 registers and instruction\$1 near5 decod\$3	1724	<u>L1</u>

END OF SEARCH HISTORY